



Rohith Rishan M

rohithrishan@gmail.com | +91 8903566021 | GitHub  | LinkedIn  | Hosur, Tamil Nadu




Professional Summary:

ECE graduate with skills in AI, Cybersecurity and VLSI. Experienced in ML deployment, RTL Design and network security. Trained in AI and Cybersecurity with strong problem-solving skills and adaptability for diverse engineering roles.



Technical Skills:

Programming Languages: Python, C, Verilog (RTL Coding)
Python Libraries: Pandas, NumPy, Matplotlib, Streamlit, Joblib, Scikit-learn
AI/ML: Model Development & Deployment, Predictive Analytics, NLP
VLSI: Digital Design, Protocols (UART), ASIC Design Flow
Cyber Security: Network Security, Threat Analysis, Access Control, Incident Response, Vulnerability Assessment
Tools & Platforms: Linux, Xilinx Vivado, ModelSim, Cisco Packet tracer, Jupyter Notebooks, VS code, Git, GitHub
Soft skills: Leadership, Collaboration, Problem Solving
Languages: English, Tamil, Hindi, Kannada

Projects:

- Employee Salary Prediction:**  Jul 2025
Languages: Python (Pandas, NumPy, Matplotlib) || ML Algorithm: Random Forest Classifier
Tools: Jupyter Notebook, Streamlit CLI
- Developed a ML model achieving ~88% accuracy in predicting salary category ($\leq 50K$ or $> 50K$) using Datasets.
 - Improved model performance through encoding and decoding, with preprocessed data from 32000+ data records, also implemented model persistence with joblib for fast prediction and a web application using Streamlit.
- Fake News Detection System:**  Jun 2025
Languages: Python (Pandas, NumPy, Scikit-learn, Streamlit) || ML Algorithm: Logistic Regression
Tools: Jupyter Notebook, Streamlit CLI
- Developed a text classification system to detect fake news with 95% accuracy using NLP
 - Deployed a Streamlit application allowing users to find news articles for instant verification as 'Real' or 'Fake'
- Implementation of High-Speed and Low-Power UART in FPGA:**  Feb 2024 – Apr 2024
HDL – Verilog || EDA Tools: Xilinx Vivado || Tool-Kit: Zybo Z7(2020) || Protocol: UART
- Designed and implemented a high-speed, low-power 8-bit UART module in an FPGA using Vivado and Zybo Z7(2020) development board
 - Utilised FIFO architecture to improve data buffering and flow control, addressing performance and power efficiency. Improved power efficiency by 38% and Speed by 15%.






Internships:

- AI/ML intern (6 Weeks) – Edunet Foundation with IBM Skillsbuild (Virtual):**  Jun 2025 – Jul 2025
Technologies and tools: AI, ML, Python Libraries, Jupyter notebook, Predictive analytics.
- Completed the AI learning path under IBM SkillsBuild with NLP, Classification and ML algorithms.
 - Built and deployed an employee salary prediction model, achieving ~88% accuracy using real-world datasets.
- Cybersecurity Intern (4 Weeks) – NIIT Foundation with Cisco (Virtual):**  Jul 2025 – Aug 2025
Technologies and Tools: Network Security(wireless), Incident Response Fundamentals, Cisco Packet Tracer
- Hands-on experience in Packet Tracer for network configuration and security simulation.
 - Completed cybersecurity essentials course and implemented a secure WLAN, achieving 100% result in Cisco Packet Tracer.

Education:

B.E., Electronics and Communication Engineering 2020-2024
K. Ramakrishnan College of Engineering, Trichy || CGPA: 8.242

Certifications:

Cisco CyberOps Associate – Cisco Network Academy 
Cybersecurity Analyst – Job Simulation (Forage) – TATA 
Artificial Intelligence Fundamentals – IBM Skillsbuild (Edunet Foundation) 
Generative AI foundation – Microsoft (upGrad) 
GenAI Powered Data Analytics – Job Simulation (Forage) - TATA 

Patents:

A System for Converting Waste into Fertilizer	Utility Model No. 20 2024 104 884	17 Sep 2024
Biodegradable Agave Sisalana Sanitary pads	2024/06446	26 Mar 2025